

ABSTRACT OF THE DISCLOSURE

A semiconductor device includes a clock input terminal to which external clocks are supplied; a PLL circuit, which is supplied with the external clocks and generate first internal clocks; a logic circuit, which operates in synchronization with the internal clocks; and an internal counter, which counts the first internal clocks when the PLL circuit is tested. The internal counter is provided with an output terminal from which an output signal thereof is supplied to an external circuit.